The Institute of OPTICS

Colloquium

University of Rochester

CEIS: Technology Transfer from the University of Rochester to New York State Industry

Eby G. Friedman

Professor of Electrical and Computer Engineering
Director of CEIS
University of Rochester

PhD UC Irvine, 1989
Hughes Aircraft 1979 - 1991
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This talk describes the Center for Electronic Imaging Systems, a NYSTAR center whose mission is the transfer of university-developed technology to New York state industry.

3:00 pm, Monday, February 25, 2008
Sloan Auditorium, Goergen Building
Refreshments following lecture

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Abstract

The Center for Electronic Imaging Systems (CEIS) is one of 15 Centers for Advanced Technology sponsored by the New York State Office of Science, Technology and Academic Research (NYSTAR) to create economic impact in the state by transferring technology from universities to industry. Funded at approximately $1 million/year, CEIS last year reported in-state revenues of over $120 million from tech transfer and business innovation projects. This exceptional performance originates from the business model used to support the technology transfer process developed here at the University. This business model and relates aspects of the Center will be discussed.

Biography

Eby G. Friedman received the B.S. degree from Lafayette College in 1979, and the M.S. and Ph.D. degrees from the University of California, Irvine, in 1981 and 1989, respectively, all in electrical engineering.

From 1979 to 1991, he was with Hughes Aircraft Company, rising to the position of manager of the Signal Processing Design and Test Department, responsible for the design and test of high performance digital and analog IC's. He has been with the Department of Electrical and Computer Engineering at the University of Rochester since 1991, where he is a Distinguished Professor, the Director of the High Performance VLSI/IC Design and Analysis Laboratory, and the Director of the Center for Electronic Imaging Systems. He is also a Visiting Professor at the Technion - Israel Institute of Technology. His current research and teaching interests are in high performance synchronous digital and mixed-signal microelectronic design and analysis with application to high speed portable processors and low power wireless communications. He is the author of more than 300 papers and book chapters, several patents, and the author or editor of nine books in the fields of high speed and low power CMOS design techniques, high speed interconnect, and the theory and application of synchronous clock and power distribution networks. Dr. Friedman is the Regional Editor of the Journal of Circuits, Systems and Computers, a Member of the editorial boards of the Analog Integrated Circuits and Signal Processing, Microelectronics Journal, Journal of Low Power Electronics, and Journal of VLSI Signal Processing, Chair of the IEEE Transactions on Very Large Scale Integration (VLSI) Systems steering committee, and a Member of the technical program committee of a number of conferences. He previously was the Editor-in-Chief of the IEEE Transactions on Very Large Scale Integration (VLSI) Systems, a Member of the editorial board of the Proceedings of the IEEE and IEEE Transactions on Circuits and Systems II: Analog and Digital Signal Processing, a Member of the Circuits and Systems (CAS) Society Board of Governors, and Program and Technical chair of several IEEE conferences, and a recipient of the University of Rochester Graduate Teaching Award, and a College of Engineering Teaching Excellence Award. Dr. Friedman is a Senior Fulbright Fellow and an IEEE Fellow.